

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	22	((in-order\$3 order\$3 sequen\$4) with (cpu processor machine)) and (((schedul\$3 execut\$3 run\$4) with (task\$3 process thread\$3 instruction)) same ((determin\$5 decid\$3 decision) with (execution processor cpu) with (width capacit\$3 bandwidth) with (order series sequen\$4 parallel\$3 concurren\$3 simultaneous\$2) with (run\$3 schedul\$3 execut\$3)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/04 08:42
S2	35	((((schedul\$3 execut\$3 run\$4) with (task\$3 process thread\$3 instruction)) same ((determin\$5 decid\$3 decision) with (execution processor cpu) with (width capacit\$3 bandwidth) with (order series sequen\$4 parallel\$3 concurren\$3 simultaneous\$2) with (run\$3 schedul\$3 execut\$3)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/03 15:54
S3	44	(((task\$3 process thread\$3 instruction) same ((second two another many plurality multiple) with fetch\$3 with (unit engine)) same (execut\$3 near4 (unit engine)) same schedul\$3) and ((processor cpu architecture) same (order\$3 series sequen\$4 parallel\$3 concurren\$3 simultaneous\$2 multithread\$3 multi-thread\$3) same (run\$3 execut\$3) same (task\$3 process thread\$3 instruction))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/03 16:04
S4	44	(((task\$3 process thread\$3 instruction) same ((second two another many plurality multiple) with fetch\$3 with (unit engine)) same (execut\$3 near4 (unit engine)) same schedul\$3) and ((processor cpu architecture) same (in-order\$3 order\$3 series sequen\$4 parallel\$3 concurren\$3 simultaneous\$2 multithread\$3 multi-thread\$3) same (run\$3 execut\$3) same (task\$3 process thread\$3 instruction))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/03 16:10
S5	42	S4 not S2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/03 16:10

S6	44	((task\$3 process thread\$3 instruction) same ((second two another many plurality multiple) with fetch\$3 with (unit engine)) same (execut\$4 near4 (unit engine)) same schedul\$3) and ((processor cpu architecture) same (in-order\$3 order\$3 series sequen\$4 parallel\$3 concurren\$3 simultaneous\$2 multithread\$3 multi-thread\$3) same (run\$3 execut\$3) same (task\$3 process thread\$3 instruction))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/03 16:16
S7	42	S6 not S2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/03 16:11
S8	44	((task\$3 process thread\$3 instruction) same ((second two another many plurality multiple) with fetch\$3 with (unit engine)) same (execut\$4 near4 (unit engine)) same schedul\$3) and ((processor cpu architecture) same (in-order\$3 dependen\$2 order\$3 series sequen\$4 parallel\$3 concurren\$3 simultaneous\$2 multithread\$3 multi-thread\$3) same (run\$3 execut\$3) same (task\$3 process thread\$3 instruction))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/03 16:17
S9	20	((in-order\$3 (von adj neuman) (von adj neuman)) with (cpu processor machine microprocessor)) same (smt multithread\$3 multi-thread\$3 (simultaneous adj multithread\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/04 08:46